L Number	Hits	Search Text	DB	Time stamp
1	1009	(crossbar$1 or (cross adj1 bar$1) or xbar$1) near8 (buffer$1 or fifo$1 or queue$1 or register$1 or cache$1 or storage$1)	USPAT	2002/12/04 05:17
2	23	((crossbar$1 or (cross adj1 bar$1) or xbar$1) near8 (buffer$1 or fifo$1 or queue$1 or register$1 or cache$1 or storage$1)) same coherenc$2	USPAT	2002/12/04 05:18

Search DL (crossbar* or xbar*) <near> (register* or buffer* or fifo* or queue*) <para

ACM Digital Library

A half century of pioneering concepts and fundamental research have been digitized and indexed in a variety of ways in this special collection of works published by ACM since its inception. The ACM Digital Library includes bibliographic information, abstracts, reviews, and full texts.

Digital Library Overview

- **What's New**
- **FAQ**
- **DL Pearls**
- **Content and Organization**
- **Terms of Usage**
- **Resources from Affiliated Organizations**

Browse the Digital Li

- **Journals**
- **Magazines**
- **Transactions**
- **Proceedings**
- **Newsletters**
- **Publications by Affil**
- **Special Interest Gro**

Personalized Services

- My Bookshelf C J C

Online Computing Re

- OCRS Ac lite Re

Search Results

Search Results for: [(crossbar* or xbar*) <near> (register* or buffer* or fifo* or queue*) <paragraph> miss]
Found 13 of 103,930 searched. → Rerun within the Portal

Search within Results

GO > Advanced Search > Search Help/Tips

Sort by: **Title** **Publication** **Publication Date** **Score** Binder

Results 1 - 13 of 13 **short listing**

1 Efficient interprocessor communication for MIMD multiprocessor systems 100%
Michel Dubois , Faye A. Briggs
Conference proceedings of the eighth annual symposium on Computer Architecture May 1981
Several interprocessor communication mechanisms for multiprocessor systems have been proposed. An efficient communication scheme must facilitate high throughput and good response time. We introduce such an efficient scheme, describe the hardware involved, and evaluate its performance. The method is based on a compile-time tagging of shared data and on using different paths for “shared” (S-) and “private” (P-) data. The S-data accesses a shared cache on a word-by-word ...

2 Performance analysis of the Alpha 21264-based Compaq ES40 system 100%
Zarka Cvetanovic , R. E. Kessler
ACM SIGARCH Computer Architecture News , Proceedings of the 27th annual international symposium on Computer architecture May 2000
Volume 28 Issue 2
This paper evaluates performance characteristics of the Compaq ES40 shared memory multiprocessor. The ES40 system contains